

SHEET 1 OF 1

**INFORMATION DISCLOSURE
CITATION IN AN
APPLICATION**

(PTO-1449)

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APPLICANT
Kentaro SHIOMI, et al.

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OFFICE OF PETITIONS

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Codes or known	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/JK/	US	6157947	12/05/2000	Watanabe et al.	_____
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EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Code-Number & Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes	Translation No
/JK/		JP 05-128187	05/25/1993	Masaharu		X	
/JK/		JP 11-224284	08/17/1999	Yoshiaki et al.		X	
/JK/		JP 10-312311	11/24/1998	Yasuto		X	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	
/JK/		Narita et al, "Board Level Simulation with R4400 Model," Study Report in Information Processing Society of Japan, Dec 1995, pp 77-84, Vol 95, No 119(DA-78)	
/JK/		Takeyama et al, "A High Speed Logic Verification System of Large Scale Digital Designs," Research Report of Information Processing Society of Japan, Jan 1995, pp 9-18, Vol 95, No 6(DA-73)	
/JK/		Notice of Reasons for Rejection issued by Japanese Patent Office (Translation)	

EXAMINER
/Jung Kim/DATE CONSIDERED
06/21/2007

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